

Refine Search

Search Results -

Terms	Documents
L25 and (instruction ADJ counter)	0

Database:	US Pre-Grant Publication Full-Text Database
	US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins
Search:	L27
	<input type="button" value="Refine Search"/>
	<input type="button" value="Recall Text"/> <input type="button" value="Clear"/> <input type="button" value="Interrupt"/>

Search History

DATE: Monday, July 23, 2007 [Purge Queries](#) [Printable Copy](#) [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
	DB=PGPB; PLUR=NO; OP=OR		
<u>L27</u>	L25 and (instruction ADJ counter)	0	<u>L27</u>
<u>L26</u>	L25 and interrupt	0	<u>L26</u>
<u>L25</u>	L24 and threshold.ab.	24	<u>L25</u>
<u>L24</u>	L23 and threshold	182	<u>L24</u>
<u>L23</u>	(717/154 717/128).ccls. or (718/103 718/108).ccls. or (711/128).ccls. or (714/47).ccls.	894	<u>L23</u>
	DB=USPT; PLUR=NO; OP=OR		
<u>L22</u>	thomson.xp. and threshold and batch	3	<u>L22</u>
<u>L21</u>	L19 and threshold.ab.	5	<u>L21</u>
<u>L20</u>	L19 and threshold	264	<u>L20</u>
<u>L19</u>	L18 and L17	264	<u>L19</u>
<u>L18</u>	L17 AND task	264	<u>L18</u>
<u>L17</u>	L15 and (swap or swapper)	346	<u>L17</u>

<u>L16</u>	L15 and interrupt	3	<u>L16</u>
<u>L15</u>	L14 and (optimize or optimizer)	3246	<u>L15</u>
<u>L14</u>	(operating adj system) and threshold	17216	<u>L14</u>
<u>L13</u>	page ADJ fault	1283	<u>L13</u>
<u>L12</u>	(717/154 717/128).ccls. or (718/103 718/108).ccls. or (711/128).ccls. or (714/47).ccls.	2841	<u>L12</u>
<u>L11</u>	(717/151 717/128).ccls. or (718/103 718/108).ccls. or (711/128).ccls. or (714/47).ccls.	2914	<u>L11</u>
<u>L10</u>	L9 not L8	8	<u>L10</u>
<u>L9</u>	L6 and (threshold).ab.	9	<u>L9</u>
<u>L8</u>	L6 and (cycles ADJ per ADJ instruction)	12	<u>L8</u>
<u>L7</u>	L6 and (cycles ADJ per ADJ instruction ADJ counter)	0	<u>L7</u>
<u>L6</u>	L5 and (optimizer or optimize or optimization)	177	<u>L6</u>
<u>L5</u>	L4 and threshold	1100	<u>L5</u>
<u>L4</u>	L3 OR L2	4519	<u>L4</u>
<u>L3</u>	cycle ADJ counter	3769	<u>L3</u>
<u>L2</u>	instruction ADJ counter	793	<u>L2</u>
<u>L1</u>	hardware ADJ performance	780	<u>L1</u>

END OF SEARCH HISTORY